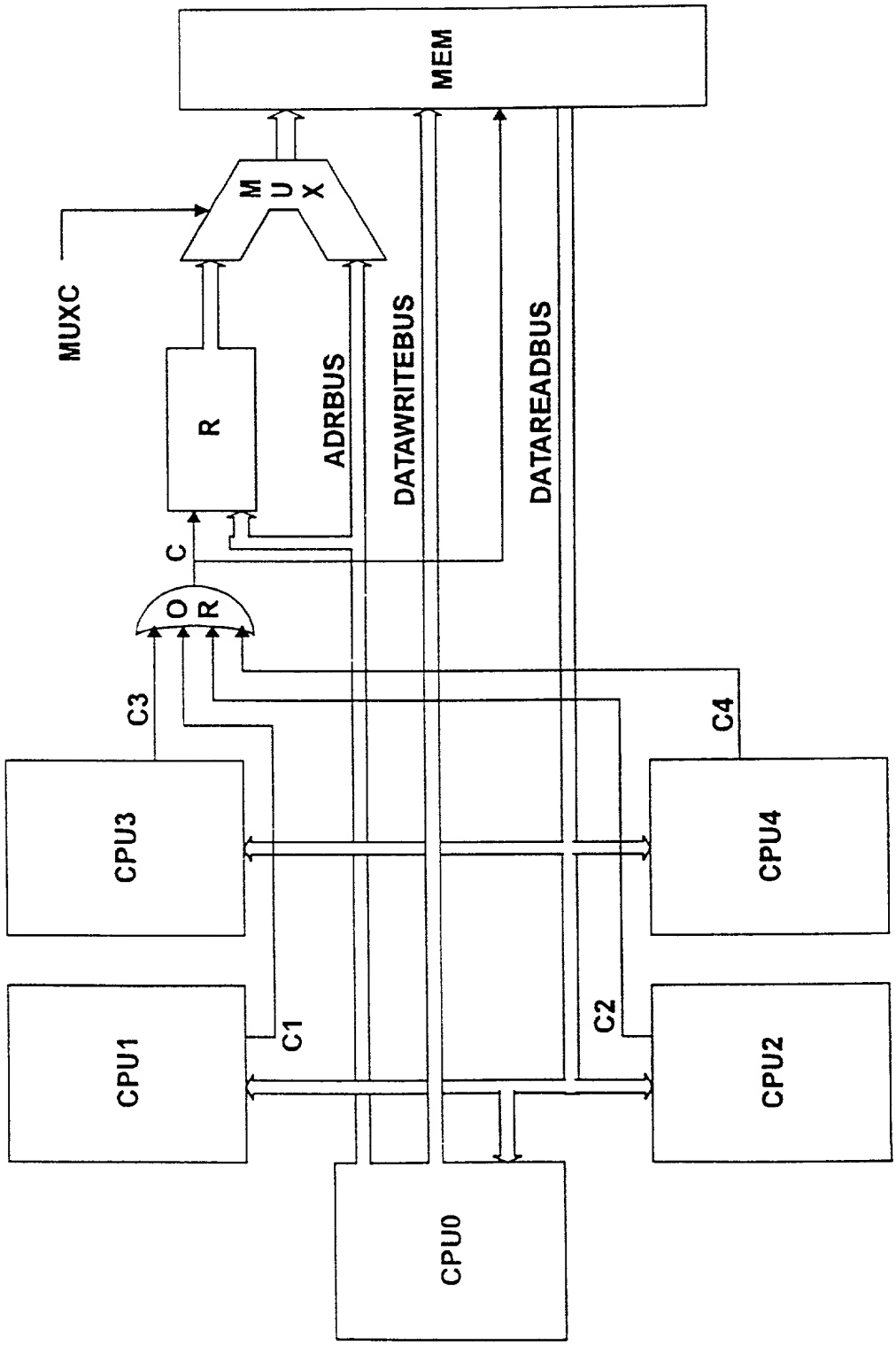
MEM
MUX
MUXC
R
ADRBUS
DATAWRITEBUS
DATAREADBUS
C
OR
C3
C4
C1
C2
CPU3
CPU4
CPU1
CPU2
CPU0